ABSTRACT OF THE DISCLOSURE

RECEIVER DECODING ALGORITHM TO ALLOW HITLESS N+1 REDUNDANCY IN A SWITCH

A switch for switching packets. The switch includes a plurality of fabrics which switch portions of packets. The switch includes a port card connected to the fabrics and the network for receiving packets from and sending packets to the network. The port card has a mechanism for tolerating whether any one of the plurality of fabrics has a failure and still sending correct packets to the network. A method for switching packets. The method includes the steps of receiving packets at a port card from a network of a switch. Then there is the step of sending to fabrics of the switch portions of the packets as stripes from the port card. Next there is the step of switching the portions of the packets with the fabrics. Then there is the step of sending back to the port card the portions of the packets as stripes from the fabrics. Next there is the step of sending correct packets with the port card to the network even though one of the fabrics has a failure.